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Ada Applications and Environments, 1988., Third International IEEE Conference on , 23-25 May 1988

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**2 A new integer linear programming formulation for the scheduling problem in data path synthesis**

*Jiahn-Hung Lee; Yu-Chin Hsu; Youn-Long Lin;*

Computer-Aided Design, 1989. ICCAD-89. Digest of Technical Papers., 1989 IEEE International Conference on , 5-9 Nov. 1989

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**3 LIP: a data path scheduler using linear integer programming**

*Jiahn-Hung Lee; Yu-Chin Hsu; Youn-Long Lin;*

VLSI Technology, Systems and Applications, 1989. Proceedings of Technical Papers. 1989 International Symposium on , 17-19 May 1989

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**4 Force-directed scheduling for the behavioral synthesis of ASICs**

*Paulin, P.G.; Knight, J.P.;*

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 8 Issue: 6 , June 1989

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**5 Optimum and heuristic data path scheduling under resource constraints**

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**6 A formal approach to the scheduling problem in high level synthesis**

*Hwang, C.-T.; Lee, J.-H.; Hsu, Y.-C.;*

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**7 Move frame scheduling and mixed scheduling-allocation for the automated synthesis of digital systems**

*Nourani, M.; Papachristou, C.;*

Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE , 8-12 June 1992

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**8 A hybrid methodology for synthesis of Petri net models for manufacturing systems**

*Zhou, M.; DiCesare, F.; Desrochers, A.A.;*

Robotics and Automation, IEEE Transactions on , Volume: 8 Issue: 3 , June 1992

Page(s): 350 -361

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**9 Language portability across shared memory multiprocessors**

*Alaghband, G.; Benten, M.S.; Jakob, R.; Jordan, H.F.; Ramanan, A.V.;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 4 Issue: 9 , Sept. 1993

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**10 Obtaining nondominated k-coteries for fault-tolerant distributed k-mutual exclusion**

*Jehn-Ruey Jiang; Shing-Tsaan Huang;*

Parallel and Distributed Systems, 1994. International Conference on , 19-21 Dec. 1994

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[\[Abstract\]](#) [\[PDF Full-Text \(532 KB\)\]](#) **IEEE CNF**

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**11 Generating and approximating nondominated coteries**

*Bioch, J.C.; Ibaraki, T.;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 6 Issue: 9 , Sept. 1995

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**12 Message complexity of the tree quorum algorithm**

*Shyan-Ming Yuan; Her-Kun Chang;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 6 Issue: 8 , Aug. 1995

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**13 Repairman models for replicated data management: a case study**

*Ing-Ray Chen; Ding-Chau Wang;*

Parallel and Distributed Information Systems, 1996., Fourth International Conference on , 18-20 Dec. 1996

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**14 Optimizing a FIFO, scalable spin lock using consistent memory**

*Rhee, I.;*

Real-Time Systems Symposium, 1996., 17th IEEE , 4-6 Dec. 1996

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**15 Transparent parallel replication of logically partitioned databases**

*Goel, R.; Shroff, G.M.;*

High Performance Computing, 1996. Proceedings. 3rd International Conference on , 19-22 Dec. 1996

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**16 A parallel solution to the cutting stock problem for a cluster of workstations**

*Nicklas, L.D.; Atkins, R.W.; Setia, S.K.; Wang, P.Y.;*

High Performance Distributed Computing, 1996., Proceedings of 5th IEEE International Symposium on , 6-9 Aug. 1996

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**17 Delay-optimal quorum consensus for distributed systems**

*Ada Waichee Fu;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 8 Issue: 1 , Jan. 1997

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**18 A prioritized multiprocessor spin lock**

*Johnson, T.; Harathi, K.;*

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**19 MPT-based branch-and-bound strategy for scheduling problem in high-level synthesis**

*Hsiao, P.-Y.; Wu, G.-M.; Su, J.Y.;*

Computers and Digital Techniques, IEE Proceedings- , Volume: 145 Issue: 6 , Nov. 1998

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**20 Consistency requirements of distributed shared memory for Lamport's bakery algorithm for mutual exclusion**

*Brzezinski, J.; Wawrzyniak, D.;*

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**21 ConC++: a concurrent C++***Bangqing Li; Baowen Xu; Hongji Yang; Chu, W.C.-C.;*

Computer Software and Applications Conference, 2000. COMPSAC 2000. The 24th Annual International , 25-27 Oct. 2000

Page(s): 223 -227

[\[Abstract\]](#) [\[PDF Full-Text \(360 KB\)\]](#) **IEEE CNF****22 Stability-based algorithms for high-level synthesis of digital ASICs***Nourani, M.; Papachristou, C.;*

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume: 8 Issue: 4 , Aug. 2000

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[\[Abstract\]](#) [\[PDF Full-Text \(148 KB\)\]](#) **IEEE JNL****23 Coterie join operation and tree structured k-coterie***Harada, T.; Yamashita, M.;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 12 Issue: 9 , Sept. 2001

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Parallel and Distributed Systems, IEEE Transactions on , Volume: 12 Issue: 11 , Nov. 2001

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Parallel and Distributed Systems, IEEE Transactions on , Volume: 13 Issue: 9 , Sept. 2002

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*Cheng-Tsung Hwang; Yu-Chin Hsu; Youn-Lang Lin;*  
Design Automation Conference, 1990. Proceedings. 27th ACM/IEEE , 24-28 June 1990  
Page(s): 65 -70

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**4 ILP formulation for data path scheduling problem with conditional branches**

*Tsuyuno, K.; Yamada, S.; Fujii, T.;*  
VLSI Signal Processing, VIII, 1995. IEEE Signal Processing Society [Workshop



on] , 16-18 Sept. 1995

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**5 MPT-based branch-and-bound strategy for scheduling problem in high-level synthesis**

*Hsiao, P.-Y.; Wu, G.-M.; Su, J.Y.;*

Computers and Digital Techniques, IEE Proceedings- , Volume: 145 Issue: 6 ,  
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*Penalba, O.; Mendias, J.M.; Molina, M.C.;*  
System Synthesis, 2000. Proceedings. The 13th International Symposium on ,  
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**2 Stability-based algorithms for high-level synthesis of digital ASICs**

*Nourani, M.; Papachristou, C.;*  
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**3 Schedulability analysis for mode changes in flexible real-time systems**

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*Li, J.; Gupta, R.K.;*  
Design, Automation and Test in Europe, 1998., Proceedings , 23-26 Feb. 1998  
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... as a control subroutine or **datapath** component ... **exclusive** after partitioning, processors are still **mutually exclusive**. ... a copy of a procedure for **exclusive** use by ...  
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... an FSM extended with **datapath operations** associated with ... partitioned into several smaller **mutually exclusive** FSMDs, as ... to its own custom **processor**, having its ...  
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... we have two smaller **mutually exclusive** processors ... with control unit and **datapath**, hypothetical switching ... partitioning into two smaller, **exclusive** processors, (h ...  
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... computing resource by **mutually exclusive operations** in scheduling ... algorithms with conditional **operations**, some scheduling ... A program for **datapath synthesis**," in ...  
[citeseer.nj.nec.com/context/229289/0](http://citeseer.nj.nec.com/context/229289/0) - 39k - [Cached](#) - [Similar pages](#)  
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[PDF]A fully bypassed six-issue integer datapath and register file on ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

... AND - OR structure, with the four **mutually exclusive** mux selects ... All mutu- ally **exclusive** mux select signals were routed ... six ALUs fit in to a **datapath** that is ...  
[cpus.hp.com/technical\\_references/jssc\\_fetzer.pdf](http://cpus.hp.com/technical_references/jssc_fetzer.pdf) - [Similar pages](#)

[PS]Scheduling Techniques to Enable Power Management Jos'e Monteiro, ...File Format: Adobe PostScript - [View as Text](#)

... The Fujitsu SPARClite **processor** provides software ... as well as the **datapath** corresponding to ... on algorithms for identification of **mutually exclusive operations**. ...  
[csg.lcs.mit.edu/~devadas/pubs/schedule.ps](http://csg.lcs.mit.edu/~devadas/pubs/schedule.ps) - [Similar pages](#)

[PDF]Techniques for Minimizing and Balancing I/O During Functional ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

... of procedures rather than arithmetic **operations** [21 ... active—in other words, the procedures are **mutually exclusive**. ... consisting of a controller and a **datapath**. ...  
[www.cs.ucr.edu/~vahid/pubs/tcad99\\_io.pdf](http://www.cs.ucr.edu/~vahid/pubs/tcad99_io.pdf) - [Similar pages](#)

[PDF]FSMD Functional Partitioning for Low PowerFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... Here, we have two smaller **mutually exclusive** processors. ... 2 Controller **Datapath Controller Datapath Processor 1 Processor** ... well as complex data **operations** in its ...  
[www.cs.ucr.edu/~ehwang/date99.pdf](http://www.cs.ucr.edu/~ehwang/date99.pdf) - [Similar pages](#)  
[\[ More results from www.cs.ucr.edu \]](#)

[PDF]Trident: A Scalable Architecture for Scalar, Vector, and Matrix ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

... of parallelism are not **mutually exclusive** and can ... **processor** consists mainly of **datapath**

circuitry and ... dependency checking by hardware between these **operations**. ...  
[crpit.com/confpapers/CRPITV6Soliman.pdf](http://crpit.com/confpapers/CRPITV6Soliman.pdf) - [Similar pages](#)

#### [PPT] Processor Modelling and Retargetable Compilation

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... primitive datatypes. -and **operations**. -appln algos. ... The topology (connectivity) of **datapath** captured by grammar attributes. Production Rules. ... **mutually exclusive**. ...  
[www.esat.kuleuven.ac.be/~mjayapal/presentations/compiler\\_talk.ppt](http://www.esat.kuleuven.ac.be/~mjayapal/presentations/compiler_talk.ppt) - [Similar pages](#)

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... The Fujitsu SPARClite **processor** provides software ... as well as the **datapath** corresponding to ... Research on Identifying **Mutually Exclusive Operations** Two **operations** ...  
[www.sigda.org/Archives/ProceedingArchives/Dac/Dac96/papers/1996/dac96/pdffiles/24\\_4.pdf](http://www.sigda.org/Archives/ProceedingArchives/Dac/Dac96/papers/1996/dac96/pdffiles/24_4.pdf) - [Similar pages](#)

#### [PPT] Chapter #12: Controller Implementation Contemporary Logic Design

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... 14 Register Transfer **operations** become 22 Microoperations: ... flexibility – complete parallel access to **datapath** control points. ... Group **mutually exclusive** signals. ...  
[www.cs.uno.edu/ClassPages/c3301/fall03/Cldch12.ppt](http://www.cs.uno.edu/ClassPages/c3301/fall03/Cldch12.ppt) - [Similar pages](#)

#### [PDF] Chapter #12: Controller Implementation

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Alternative Ways to Implement **Processor** FSMs • "Random ... ROM, decoded externally some **mutually exclusive** signals can ... 14 Register Transfer **operations** become 22 ...  
[www.cs.uno.edu/ClassPages/c3301/fall03/Cldch12.pdf](http://www.cs.uno.edu/ClassPages/c3301/fall03/Cldch12.pdf) - [Similar pages](#)

#### [PDF] Multiple Behavior Module Synthesis Based on Selective Groupings

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... The simplicity of the **operations** makes P class instructions easily ... We also assume they are **mutually exclusive** to one another ... 6, ie, a **datapath** for DFG-A and DFG ...  
[jamaica.ee.pitt.edu/Archives/ProceedingArchives/Date/Date98/papers/1998/date98/pdffiles/05a\\_3.pdf](http://jamaica.ee.pitt.edu/Archives/ProceedingArchives/Date/Date98/papers/1998/date98/pdffiles/05a_3.pdf) - [Similar pages](#)

#### [PDF] FSMD Functional Partitioning for Low Power

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... 2. Here, we have two smaller **mutually-exclusive** processors. ... The **datapath** of **processor** 1 is not consuming power ... types, as well as complex data **operations** in its ...  
[jamaica.ee.pitt.edu/Archives/ProceedingArchives/Date/Date99/papers/1999/date99/pdffiles/01b\\_1.pdf](http://jamaica.ee.pitt.edu/Archives/ProceedingArchives/Date/Date99/papers/1999/date99/pdffiles/01b_1.pdf) - [Similar pages](#)  
[\[ More results from jamaica.ee.pitt.edu \]](#)

#### Bridge

... Instruction Set **Processor** Specifications (ISPS): The Notation and Its Applications ... Pizarro , Mitch Mlinar, MAHA: a program for **datapath** synthesis, Proceedings ...  
[www.acm.org/pubs/citations/proceedings/dac/285730/p415-tseng/](http://www.acm.org/pubs/citations/proceedings/dac/285730/p415-tseng/) - 34k - [Cached](#) - [Similar pages](#)

#### [PDF] EE182 Computer Organization and Design Winter 1998 Chapter 5 ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Cycle Implementation Slide 1 EE 182 – Winter 1989 EE182 Computer Organization and Design Winter 1998 Chapter 5 Lectures **Processor Datapath** and Control Part II ...  
[www.cs.huji.ac.il/~comarc/slides/Chapter5\\_2.pdf](http://www.cs.huji.ac.il/~comarc/slides/Chapter5_2.pdf) - [Similar pages](#)

#### Chapter Nine

... These **operations** can be further decomposed into 22 ... be ruled out by the **datapath** bussing strategy ... can group the control signals into **mutually exclusive** sets to ...  
[www.ice.cycu.edu.tw/~vlsi/Chapter9/Chapter9.htm](http://www.ice.cycu.edu.tw/~vlsi/Chapter9/Chapter9.htm) - 31k - [Cached](#) - [Similar pages](#)

#### Optimization Efficiency in Behavioural Synthesis

... layout of the system, the **datapath** has 54 ... data path units activated by the **operations** in the control steps are shared unless they are **mutually exclusive**. ...  
[www.ecs.soton.ac.uk/publications/rj/1994/dag/baker/paper3.html](http://www.ecs.soton.ac.uk/publications/rj/1994/dag/baker/paper3.html) - 20k - [Cached](#) - [Similar pages](#)

#### [PDF] Good morning

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... as well as on **mutually exclusive operations** in different ... multiplexers, control logic, and **datapath** logic including ... pipelines of loops, **operations**, and functions ...  
[www.edacafe.com/technical/papers\\_pdf/get2chip1.pdf](http://www.edacafe.com/technical/papers_pdf/get2chip1.pdf) - [Similar pages](#)

#### [PPT] Chapter 12 Control Implementation

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... 14 Register Transfer **operations** become 22 Microoperations: ... most flexibility – parallel

access to **datapath** control points. ... Group **mutually exclusive** signals. ...  
mobilenet.snu.ac.kr/COURSES/DigitalLogic/ 2003/chap12.ppt - [Similar pages](#)

**[PDF]Processor Design MU0 Instruction Set Architecture**

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... you) can verify the validity of the **datapath** by testing the ... within the **processor** which are used for internal **operations**. ... AND A and B EOR Bitwise **exclusive-OR** A ...  
www.cs.man.ac.uk/teaching/electronics/ CS121/proc\_des4.pdf - [Similar pages](#)

**[PS]University of Manchester**

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... you) can verify the validity of the **datapath** by testing ... As these two operations are **mutually exclusive** it is ... the same wires for the data in both **operations**. ...  
www.cs.man.ac.uk/teaching/electronics/ CS121/proc\_des4.ps - [Similar pages](#)  
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**[PS]1**

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... 8i1;i2 2 I;(i1;i2) **mutually exclusive**. ... wrote the specification of a 32-bit **datapath**, RISC-style ... load, store, jump, ALU, multiply and variable shift **operations**. ...  
www.cag.lcs.mit.edu/~rinard/paper/aspdac01.ps - [Similar pages](#)

**[PS]High-level Specification and Efficient Implementation of ...**

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... We wrote the specification of a 32-bit **datapath**, RISC-style, linearly pipelined **processor** ... variable shift **operations**. ... 8i1; i2 2 I; (i1; i2) **mutually exclusive**. ...  
www.cag.lcs.mit.edu/~cristina/ research/papers/asp-dac01.ps - [Similar pages](#)  
[ [More results from www.cag.lcs.mit.edu](#) ]

**[PDF]Acrobat Distiller, Job 50**

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EECS150 Section 10 Controller Implementations Fall 2001 EECS150 - Fall 2001 1-2  
Implement of **Processor** FSMs Classical Finite State Machine Design Divide and ...  
organics.eecs.berkeley.edu/~viveks/ cs150/lectures/section10.pdf - [Similar pages](#)

**[PS]Copyright**

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... fine-grainness and flexibility are **mutually exclusive** properties; we ... necessary to span the entire I/O **datapath**). ... Any scalar data **operations** will take multiple ...  
www.cse.psu.edu/~ugrain/vlsi-cad/MGAP/frontiers95.ps - [Similar pages](#)

**[PDF]Compiler-Directed ILP Extraction for Clustered VLIW/EPIC machines ...**

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horizon.ece.utexas.edu/~jacome/nova/ Publications/SatishModulo.pdf - [Similar pages](#)

**[PPT]www.cs.plu.edu/courses/csce480/notes/micro\_17\_cpu3.ppt**

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... The **datapath** in a CPU is broken up by ... programming" at the level of register transfer **operations**. ... GOAL: group **mutually exclusive** signals into a binary code and ...  
[Similar pages](#)

**[PS]INSTRUCTION-LEVEL PARALLELISM IN ASYNCHRONOUS PROCESSOR ...**

File Format: Adobe PostScript - [View as Text](#)

... The **operations** of each of the stages are further exposed ... is arbitrated by the CU through **mutually-exclusive** assertions of ... Cycle Time (ICT) **Datapath** Latency (MDL ...  
www.dcs.ed.ac.uk/home/dka/belgium94.ps - [Similar pages](#)

**[DOC]Proceedings Template - WORD**

File Format: Microsoft Word 2000 - [View as HTML](#)

... developed the architecture view which separates the control from the **datapath**. ... Due to resource sharing, most of these **operations** are **mutually exclusive**. ...  
www-cad.eecs.berkeley.edu/~sjweber/DAC\_paper\_v1.11.doc - [Similar pages](#)

**[PDF]Evaluation and Guidance in Processor Architecture Selection for ...**

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... Evaluation and Guidance in **Processor** Architecture Selection for DSP 1 Chapter 1 ... signal processing (DSP), the use of mathematical **operations** and algorithms to ...  
www-cad.eecs.berkeley.edu/HomePages/naji/research/ phd\_dissertation/Dissertation.pdf - [Similar pages](#)

**[PDF]An Emulator for Exploring Coarse-Grained Configurable ...**



File Format: PDF/Adobe Acrobat - View as HTML

... zero time so that arbitrarily many **operations** can be ... the data stream and sent to the **datapath**. ... read and write address spaces were always **mutually exclusive**. ... [www.bergs.com/stefan/papers/fpl01.pdf](http://www.bergs.com/stefan/papers/fpl01.pdf) - [Similar pages](#)

[PDF]Introduction

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... reducing the maximum number of serial **operations** in the ... It implements the signi cand **datapath** in two ... normalizing left shift to be **mutually exclusive** with only ... [www.jucs.org/jucs\\_4\\_2/the\\_snap\\_project\\_building/paper.pdf](http://www.jucs.org/jucs_4_2/the_snap_project_building/paper.pdf) - [Similar pages](#)

[PDF]Exploiting Fine-Grain Parallelism of IDEA Using Xilinx FPGA

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... IDEA each round has many **mutually exclusive operations**, which could ... The three **operations** along with the pipeline registers ... Figure 3 shows the **datapath** of our ... [webpages.uah.edu/~gurumas/publication1.pdf](http://webpages.uah.edu/~gurumas/publication1.pdf) - [Similar pages](#)

[PDF]Multi-view Operation-level Design – Supporting the Design of ...

File Format: PDF/Adobe Acrobat - View as HTML

... multiple specifications of a single **processor** conform to ... interesting configurations of the **datapath** described in ... To extract the **operations**, we first translate ... [https://www.gigascale.org/pubs/384/ERL\\_tech\\_report.pdf](https://www.gigascale.org/pubs/384/ERL_tech_report.pdf) - [Similar pages](#)

[PPT]Computer Systems Overview

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... No. Structure of a Computer. **Datapath**. ALU Block Diagram. ... Decouple memory system from. internal **processor** operation. ... Diagram. Register transfer **operations**: <Reads>. ... [supercom.yonsei.ac.kr/class/DS003-2003/lecture/cs-chap1.ppt](http://supercom.yonsei.ac.kr/class/DS003-2003/lecture/cs-chap1.ppt) - [Similar pages](#)

[PS]Controller Implementation (C) K. Nickels - No. 12-1 Chapter #12: ...

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... Reset, Current State, a, b) Next State Register Transfer **Operations**. ... **Datapath** Control. ... signals in ROM, decoded externally some **mutually exclusive** signals can be ... [www.engr.trinity.edu/~knickels/class/engr4365/f98/handouts/ch12.ps](http://www.engr.trinity.edu/~knickels/class/engr4365/f98/handouts/ch12.ps) - [Similar pages](#)

[PS]IEICE TRANS. FUNDAMENTALS, VOL. E00-A, NO. 3 MARCH 1998 1

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... P 1. In addition, either operation 8 or operation 16 also shares the **processor** with the **mutually exclusive** operation 14. ... **datapath** synthesis," in Proc. ... [www.elc.ees.saitama-u.ac.jp/~kazuhito/paper/IEICE98.ps](http://www.elc.ees.saitama-u.ac.jp/~kazuhito/paper/IEICE98.ps) - [Similar pages](#)

[PDF]Determining the Order of Processor Transactions in Statically ...

File Format: PDF/Adobe Acrobat - View as HTML

... order for a given **processor** assignment, actor ... all the three scheduling **operations** — assigning actors ... The classic controller/**datapath** architecture is also in ... [ptolemy.eecs.berkeley.edu/publications/papers/97/order/order.pdf](http://ptolemy.eecs.berkeley.edu/publications/papers/97/order/order.pdf) - [Similar pages](#)

[PDF]Automatic architectural synthesis of VLIW and EPIC processors

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... addsubw\_2 gpr pr Figure 3. A **datapath** example ... For predi-cated **operations**, a separate predicate input ... within an operation group are **mutually exclusive** while, by ... [www.hpl.hp.com/research/itc/car/papers/Architectural.pdf](http://www.hpl.hp.com/research/itc/car/papers/Architectural.pdf) - [Similar pages](#)

[PDF]ShiftQ: A Buffered Interconnect for Custom Loop Accelerators

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... is generated by customizing control **datapath** and storage ... and at what time these **operations** should occur ... Accelerators Figure illustrates a **processor** that uses a ... [www.hpl.hp.com/techreports/2001/HPL-2001-255.pdf](http://www.hpl.hp.com/techreports/2001/HPL-2001-255.pdf) - [Similar pages](#)  
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Microprogramming History – Mark Smotherman

... encoding one of several **mutually exclusive** choices (eg ... cycle in which the **datapath** microoperations are ... An alternative approach to multiple **operations** per cycle ... [www.cs.clemson.edu/~mark/uprog.html](http://www.cs.clemson.edu/~mark/uprog.html) - 50k - [Cached](#) - [Similar pages](#)

[PDF]HDLGen: Architecture Description Language driven HDL Generation ...

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... 6.3 Data Path The implementation of **datapath** consists of two parts. ... using a bus, and load and store **operations** that are **mutually exclusive** with the move. ... [www.cecs.uci.edu/technical\\_report/TR03-04.pdf](http://www.cecs.uci.edu/technical_report/TR03-04.pdf) - [Similar pages](#)

[PDF]Microsoft PowerPoint - Compilers and DSPs.pptFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... scheduling) Phases considered **mutually independent** Each ... How to Exploit Registers in of Pipelined **Datapath**? Chained **operations**: "Pipelined" MAC Many special ...  
[lapwww.epfl.ch/courses/conproav/ Compilers%20and%20DSPs.pdf](http://lapwww.epfl.ch/courses/conproav/Compilers%20and%20DSPs.pdf) - [Similar pages](#)

[PDF]G22.3033-004 Architecture and Programming of Parallel Computers ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

... 1): Data Parallel • Programming model – **operations** performed in ... little memory  
 – a control **processor** issues instructions ... only 32-bit **datapath** slices would ...  
[www.cs.nyu.edu/courses/fall01/G22.3033-004/ lectures/lect2.pdf](http://www.cs.nyu.edu/courses/fall01/G22.3033-004/lectures/lect2.pdf) - [Similar pages](#)

[PS]HDLGen: Architecture Description Language driven HDLFile Format: Adobe PostScript - [View as Text](#)

... The implementation of **datapath** consists of two parts. ... that is implemented using a bus, and load and store **operations** that are **mutually exclusive** with the ...  
[www.ics.uci.edu/~pmishra/Publications/hdlgenTR.ps](http://www.ics.uci.edu/~pmishra/Publications/hdlgenTR.ps) - [Similar pages](#)

[doc]\documentstyle[fullpage,12pt]{article}File Format: Microsoft Word 97 - [View as HTML](#)

... in High Performance **Processor Architectures**," (with ... Regularity Extraction in **Datapath Circuits**," (with ... to Determine **Mutually Exclusive Operations** in Behavioral ...  
[www.ics.uci.edu/~rgupta/Vita2002.doc](http://www.ics.uci.edu/~rgupta/Vita2002.doc) - [Similar pages](#)  
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Details Sen

... state machine design; **datapath** functions, counters ... **Mutually exclusive** with: U2.043  
 Computer Science ... Accounting and management; **Operations** management; Marketing ...  
[www.eelab.usyd.edu.au/ee\\_database/ basic/detailsSen.html](http://www.eelab.usyd.edu.au/ee_database/basic/detailsSen.html) - 24k - [Cached](#) - [Similar pages](#)

[PDF]Automated Composition of Hardware ComponentsFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... component communication. The collection of **operations** necessary to perform a data transfer is referred to as a function. 4.1 Component ...  
[akebono.stanford.edu/users/nanni/ research/sys/inter\\_dac98.pdf](http://akebono.stanford.edu/users/nanni/research/sys/inter_dac98.pdf) - [Similar pages](#)

[PDF]CS130 Exam # 3 Review NotesFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... of a "Computer" - **Processor** - Control - **Datapath** (ALU) - Memory ... SUB, DIV, MUL  
 - Logical **Operations** : AND, OR ... checkbox is NOT **mutually exclusive** | radio is ...  
[www.ece.mtu.edu/faculty/btdavis/ courses/wu\\_cs130/ex3.review.pdf](http://www.ece.mtu.edu/faculty/btdavis/courses/wu_cs130/ex3.review.pdf) - [Similar pages](#)

[PDF]EECS 416 Study OutlineFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... 1 Meg(w/ 16 bit **datapath**) - Concurrent 64k ... Chip Select Logic - Distinct & **mutually exclusive** select for ... as decoder - Able to define logical **operations** for each ...  
[www.ece.mtu.edu/faculty/btdavis/courses/ ut\\_eecs416/studyoutline.fm.pdf](http://www.ece.mtu.edu/faculty/btdavis/courses/ut_eecs416/studyoutline.fm.pdf) - [Similar pages](#)

[PDF]Design Methodology and Optimization TechniquesFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... quad-rail signals, or other **Mutually Exclusive** Assertion Groups ... Logic, Inc., which began **operations** in Minnesota ... determining the worse-case **datapath** delay and ...  
[www.ece.umn.edu/~smithsco/dissertation.pdf](http://www.ece.umn.edu/~smithsco/dissertation.pdf) - [Similar pages](#)

[PS]EE216A Lecture 6 1File Format: Adobe PostScript - [View as Text](#)

... be what you want, especially is cases labels are **mutually exclusive**. ... This is called the **datapath** portion of the chip Where ... Used to represent **operations** on multi ...  
[www.janet.ucla.edu/~ingrid/ee216a/ lect06\\_verilog\\_modified.ps](http://www.janet.ucla.edu/~ingrid/ee216a/lect06_verilog_modified.ps) - [Similar pages](#)

[PDF]Microsoft PowerPoint - L3File Format: PDF/Adobe Acrobat - [View as HTML](#)

... types of temporal restrictions (not **mutually exclusive**) 3 Maximum ... in a **datapath** 3  
 Computation being scheduled – may ... in a process network, or **operations** in a ...  
[www.janet.ucla.edu/~mbs/courses/ee298-2/ 1998f/lectures/L3\\_6pp.pdf](http://www.janet.ucla.edu/~mbs/courses/ee298-2/1998f/lectures/L3_6pp.pdf) - [Similar pages](#)  
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[PPT]CPU structureFile Format: Microsoft Powerpoint 97 - [View as HTML](#)

... idea is to split the **datapath** into stages ... Less flexible because **mutually exclusive**

signals must be identified ... still be done by using several simpler **operations**. ...

jjo.free.net.ph/programming/school/cmcs-132/lect09-cpu\_structure\_and\_function.ppt - [Similar pages](#)

#### [PDF]Target Architecture Oriented High-Level Synthesis for Multi-FPGA ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... during synthesis (c) Generated partitioned **datapath** cut-line ... the potential sharing of **mutually exclusive** resources ... the time frames, for all **operations**  $op \in OP$  ...

www.fzi.de/sim/publications/2000001-paper.pdf - [Similar pages](#)

#### [PPT]Chapter #12: Controller Implementation Contemporary Logic Design ...

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... 14 Register Transfer **operations** become 17 Microoperations: ... complete parallel access to **datapath** control points. ... PC, PC + 1 -> PC, ABUS -> PC **mutually exclusive**. ...

sdgroup.snu.ac.kr/class/digital.2001/note/302L07.ppt - [Similar pages](#)

#### [PDF]Compilation Increasing the Scheduling Scope for Multi- Memory- ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... herein, designated by region) in the global DFG to generate **datapath** solutions with ... of each multiplier and one clock cycle for the other **operations**, the region ...

w3.ualg.pt/~jmcardo/MyPapers/fpl01.pdf - [Similar pages](#)

#### [PDF]Petri Nets 2000

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... part of the algorithms • **Datapath operations** may have ... event annotation (from abstract **operations** down to ... and abstract refinement; **processor** example • Low ...

www.daimi.au.dk/pn2000/proceedings/ pn2000\_hardwaretut.pdf - [Similar pages](#)

#### [PPT]specdecode

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... 64-bit **datapath**. 64-bit data buffer. ... 64KB I-DL1, 512KB unified L2. 2 load / 1 store ports (**mutually exclusive**). ... A store is converted implicitly into 3 **operations**. ...

www.ece.wisc.edu/~pharm/talks/ISCA-29-SPECDECODE.ppt - [Similar pages](#)

#### [PDF]Computer: PicoRadio Supports Ad Hoc Ultra-Low Power Wireless ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... addressed the partitioning of **operations** between the ... DSPs Dedicated DSP Reconfigurable **datapath** RF filter ... must meet two usually **mutually exclusive** goals: fast ...

bwrc.eecs.berkeley.edu/Background/IEEE%20Computer%20Magazine%20Article/ r7042b.pdf - [Similar pages](#)

#### [PS]In Proceedings, IEEE/ACM International Conference on Computer- ...

File Format: Adobe PostScript - [View as Text](#)

... RTL Circuit(controller + **datapath**) Test bench with typical input traces. ... AND **operations**, respectively. ... since the decoded state variables are **mutually exclusive**. ...

www.ee.princeton.edu/~anand/pub\_links/iccad96\_hlest.ps - [Similar pages](#)

#### Abstracts DATE 98

... The **datapath** word-length has been selected ... algorithm development, new **processor** and memory ... Algorithm to Determine **Mutually Exclusive Operations** in Behavioral ...

ballade.cs.ucla.edu/~kohcc/sigdacdrom/date98/papers/ 1998/date98/htmlfiles/sun\_sgi/frames/dateabs.htm - 101k - [Cached](#) - [Similar pages](#)

#### [PDF]Complex Streamed Media Processor Architecture

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... executed VIS instruc- tions are instructions associated with overhead **operations**, such as ... pressure on the decode/issue logic of a superscalar **processor** and can ...

ce.et.tudelft.nl/publicationfiles/ 682\_5\_Dmitry\_bw\_13-02\_changed.pdf - [Similar pages](#)

#### [PDF]Lecture 6: High-Level Logic Design Hardware Description Languages

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... you want, especially is cases labels are **mutually exclusive**. ... Dataflow This is called the **datapath** portion of ... important • Used to represent **operations** on multi ...

www2.ele.ufes.br/~ailson/microeletronica/ stanford\_ee271/lect\_06.pdf - [Similar pages](#)

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... BO/BI, SH, and CRF channels are **mutually exclusive** with respect ... 4.2 Logical **Operations** Logic is very simple ... requires not just a single **datapath**, but sequencing ...

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... DRAM) as the basis for a fast cache in the context of an asynchronous **processor**. ... Two transistor(2T) memory cell A DRAM with a one-bit wide **datapath** is formed ...  
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... vi IXP1200 Network **Processor** Hardware Reference Manual 5.3 ... 6.3.1 Read and Write **Operations**.....6-8 ...  
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... to the CPU **datapath** and functional ... simultaneously fetch many **mutually exclusive**, simple instructions ... of simultaneous multithreading, **operations** from multiple ...  
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United States Patent Application: 0020133784

... groups that are marked as **mutually exclusive** may be ... these design guidelines, the **datapath** synthesizer attempts ... operation groups that are **exclusive**, and places ...  
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... the right places, and that no **datapath** or computational ... simultaneously, because they each require **exclusive** access to ... in each subgraph are **mutually compatible**. ...  
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... dispatch times: The maximum time from the **processor** recognizing the ... classification: three types of temporal restrictions (not **mutually exclusive**) Maximum: no ...  
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... three types of temporal restrictions (not **mutually exclusive**). ... Functions or **operations** called actions may be executed ... an internal or external **datapath** or object ...  
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... an instruc- tion is decoded into multiple **operations**. ... port, load/store ports are **mutually exclusive** Table 2 ... 404 Cache / Memory 64-bit **datapath** Bypass Network ...  
systems.cs.colorado.edu/ISCA2002/FinalPapers/ ikim\_isca2002\_cameraready.pdf - [Similar pages](#)

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... **processor** into multiple simpler **mutually-exclusive** communicating processors ... only one smaller **processor** is active ... a complete set of **datapath operations** that can ...  
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... fast bit permutation, n-dimensional matrix masking **operations**. ... some linked to the real **datapath**, all doing ... they have an embedded **processor**, several megabytes ...  
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... Bth sample time, exactly enough **operations** would have ... fact, the characteristics are not **mutually exclusive**; it is ... and the DSP comprise the **datapath**, while the ...  
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... was possible because **mutually exclusive operations** could be ... definition of **OPERATIONS ALLOCATION SCHEDULING**. ... WFJ.Verhaegh, Flexible **Datapath** Compilation for ...  
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... More details of the ARM processor are given in ... load (right hand sides) and store operations (left hand ... into eight classes according to their datapath activities ...  
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... eight classes according to their datapath activities ... Mutually exclusive requests from the execution unit to the ... Exclusive access to the MAR is thus guaranteed at ...  
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... give us a better estimate of the datapath area ... per- form enclosed operations in a mutually exclusive manner ... Operations in dif- ferent branches of an if statement ...  
[www.cse.iitd.ernet.in/esproject/homepage/docs/projects/2001-2002/hw\\_est/thesis.ps](http://www.cse.iitd.ernet.in/esproject/homepage/docs/projects/2001-2002/hw_est/thesis.ps) - [Similar pages](#)

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... & Power Models RTL Circuit (controller + datapath) Test bench ... Q represent the Boolean OR and AND operations, respectively ... circuit that is a pre-processor for a ...  
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... We could have optimized the datapath to sup ... Due to the mutually exclusive relationship between data in L1 ... are fully pipelined, and cache operations are lockup ...  
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... datapath. ... to reiterate that SSMT microthread en-hancements are not mutually exclusive. ... Informing memory operations: Providing memory performance feedback in ...  
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... are mutually exclusive. ... Dataflow. This is called the datapath portion of the chip. ... Used to represent operations on multi-bit data. ...  
[panda.snu.ac.kr/CourseWork/vlsi/Lecture%206.ppt](http://panda.snu.ac.kr/CourseWork/vlsi/Lecture%206.ppt) - [Similar pages](#)

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... The processor datapath includes 24 general registers and 12 ... of the actions and coding the operations of the ... is the management of mutually-exclusive writing to ...  
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